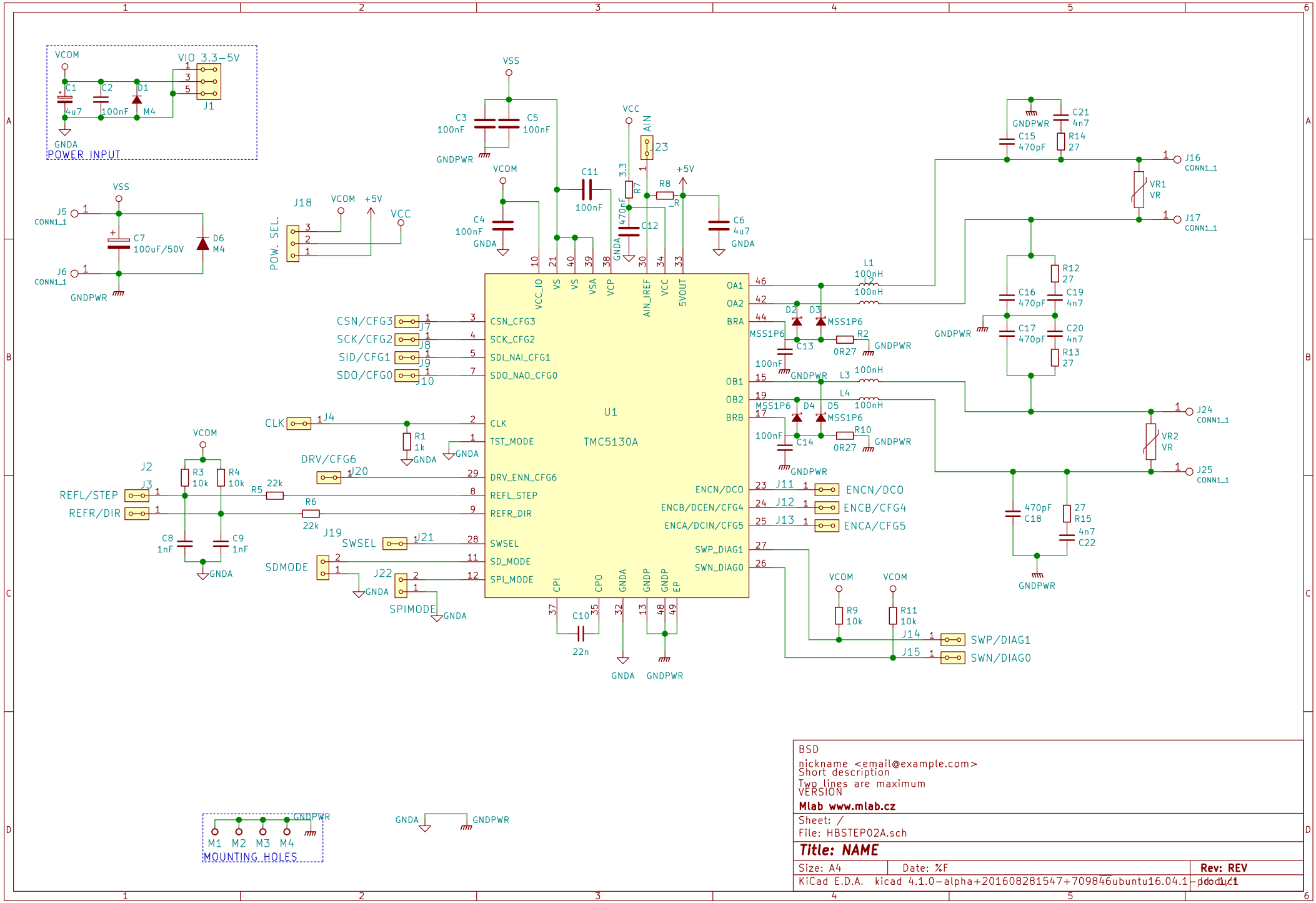
POWER INPUT
VCOM
VIO 3.3-5V
C1 4u7
C2 100nF
D1 M4
J1
GNDA
J5 CONN1_1
J6 CONN1_1
VSS
C7 100uF/50V
D6 M4
GNDPWR
J18
POW. SEL.
VCOM +5V
VCC
C3 100nF
C5 100nF
GNDPWR
C4 100nF
GNDA
C11 100nF
C12 470nF
R7 3.3
R8 _R
AIN
J23
+5V
C6 4u7
GNDA
U1
TMC5130A
VCC_IO
VS
VSA
VCP
AIN_IREF
5VOUT
OA1
OA2
BRA
OB1
OB2
BRB
CSN_CFG3
SCK_CFG2
SDI_NAI_CFG1
SDO_NAO_CFG0
CLK
TST_MODE
DRV_ENN_CFG6
REFL_STEP
REFR_DIR
SWSEL
SD_MODE
SPI_MODE
ENCN/DCO
ENCB/DCEN/CFG4
ENCA/DCIN/CFG5
SWP_DIAG1
SWN_DIAG0
CPI
CPO
GNDP
EP
J7 CSN/CFG3
J8 SCK/CFG2
J9 SID/CFG1
J10 SDO/CFG0
J4 CLK
R1 1k
J20 DRV/CFG6
J2
J3 REFL/STEP
REFR/DIR
R3 10k
R4 10k
R5 22k
R6 22k
C8 1nF
C9 1nF
J19 SWSEL
J21
SDMODE
J22
SPIMODE
C10 22n
J11 ENCN/DCO
J12 ENCB/CFG4
J13 ENCA/CFG5
J14 SWP/DIAG1
J15 SWN/DIAG0
R9 10k
R11 10k
L1 100nH
L2 100nH
L3 100nH
L4 100nH
D2 MSS1P6
D3 MSS1P6
D4 MSS1P6
D5 MSS1P6
R2 0R27
R10 0R27
C13 100nF
C14 100nF
C21 4n7
C15 470pF
R14 27
J16 CONN1_1
VR1 VR
J17 CONN1_1
R12 27
C16 470pF
C19 4n7
C17 470pF
C20 4n7
R13 27
J24 CONN1_1
VR2 VR
J25 CONN1_1
470pF C18
27 R15
4n7 C22
MOUNTING HOLES
M1 M2 M3 M4
BSD
nickname <email@example.com>
Short description
Two lines are maximum
VERSION
Mlab www.mlab.cz
Sheet: /
File: HBSTEP02A.sch
Title: NAME
Size: A4
Date: %F
Rev: REV
KiCad E.D.A. kicad 4.1.0-alpha+201608281547+709846ubuntu16.04.1-product
Id: 1/1